



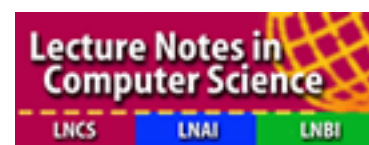
Thanks to all participants of ARC2017! Keep an eye on [the main symposium website](#) for information about the 2018 edition.

Welcome to ARC2017, the 13th International Symposium on Applied Reconfigurable Computing

Reconfigurable computing technologies offer the promise of substantial performance gains over traditional architectures via customizing the topology of the underlying architecture to match the specific needs of a given application, even at run-time. Contemporary configurable architectures allow for the definition of architectures with functional and storage units that match in function, bit-width and control structures the specific needs of a given computation. The flexibility enabled by reconfiguration is also seen as a basic technique for overcoming transient failures in emerging device structures. The International Symposium on Applied Reconfigurable Computing (ARC) aims to bring together researchers and practitioners of reconfigurable computing with an emphasis on practical applications of this promising technology.

This year's Symposium will have a series of international invited speakers who will express their views on the future of reconfigurable technology. The accepted papers will be included in the proceeding published by Springer Lecture Notes in Computer Science (LNCS), which will be indexed by ISI Proceedings and EI-Compendex.

Selected papers will be invited to be submitted for consideration in the special issue "Recent Progress in Applied Reconfigurable Computing" of Elsevier Journal of Microprocessors and Microsystems (MICPRO).



THEMES

Authors are invited to submit original contributions in English on a wide variety of topics related to applied reconfigurable computing, including but not limited to those listed below.

Design Methods & Tools

- High-Level Languages and Compilation
- Simulation and Synthesis
- Estimation Techniques
- Design Space Exploration
- Run-Time Systems and Virtualization

Architectures

- Self-adaptive, evolvable
- Heterogeneous (GPU, APU)
- Low-Power Designs
- Approximate Computing
- Fine-/Coarse-/Mixed-grained
- Embedded MPSoCs and NoCs
- Resilient and Fault Tolerant

Applications

- Security and Cryptography
- Big Data and HPC
- Embedded Computing and DSP
- Robotics, Space, Bioinformatics, Automotive
- Safety and Mission-Critical Systems

Trends (in)

- Teaching RC
- Surveys and Future Trends
- Benchmarks
- Emerging Technologies

DEADLINES AND DATES

Event:	Monday April 3rd to Friday April 7th, 2017
Symposium:	Tuesday April 4, 2017 Wednesday April 5, 2017 Thursday April 6, 2017
Tutorials:	Monday April 3, 2017 Friday April 7, 2017
Submission:	Friday November 11, 2016
Submission (extended):	Friday November 25, 2016 (hard deadline)
Author notification:	Monday December 19, 2016
Camera-ready:	Friday January 20, 2017

PREVIOUS ARC SYMPOSIUMS